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Processor having multiple program counters and trace buffers outside an execution pipeline

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Inventor(s): AKKARY HAITHAM [US]; CHOW KINGSUM [HK] +

Applicant(s): INTEL CORP [US] +

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Abstract of **TW 406239 (B)**

In one embodiment of the invention, a processor includes an execution pipeline to concurrently execute at least portions of threads, wherein at least one of the threads is dependent on at least another one of the threads. The processor also includes detection circuitry to detect speculation errors in the execution of the threads. In another embodiment, the processor includes thread management logic to control dynamic creation of threads from a program.

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